Fig.1 (Related Art)

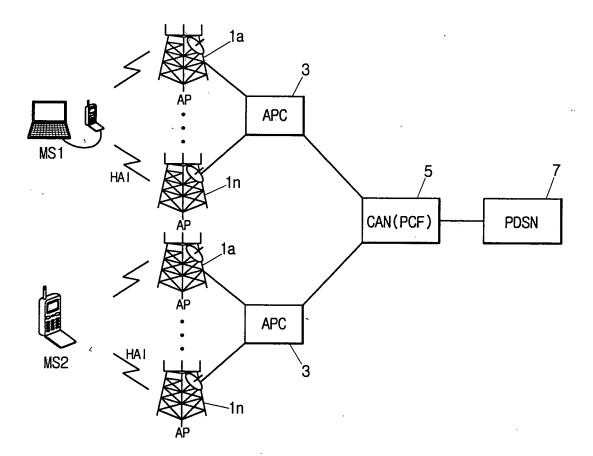


Fig.2 (Related Art)

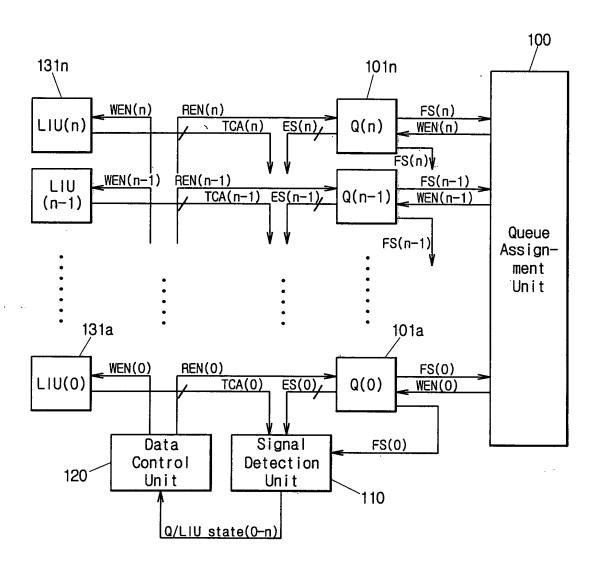
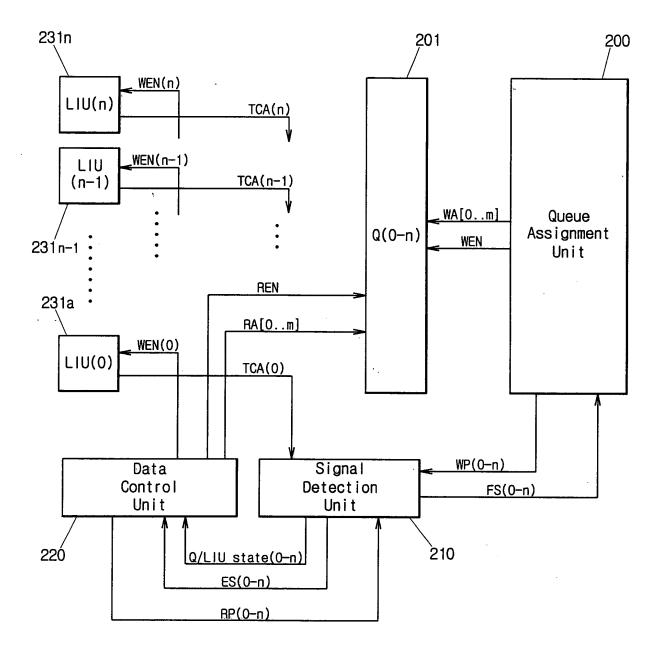
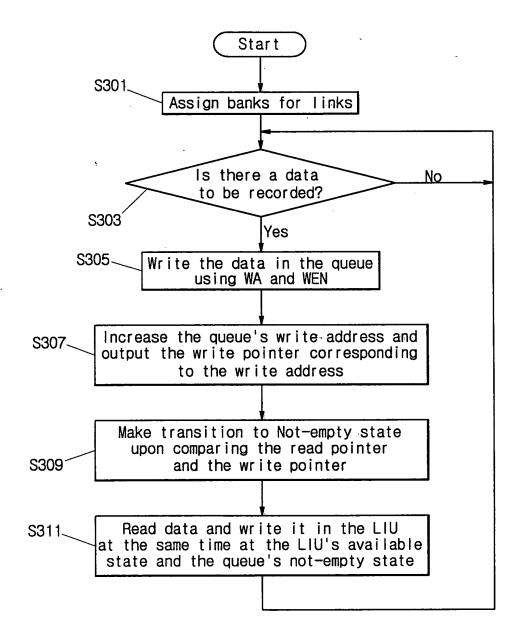
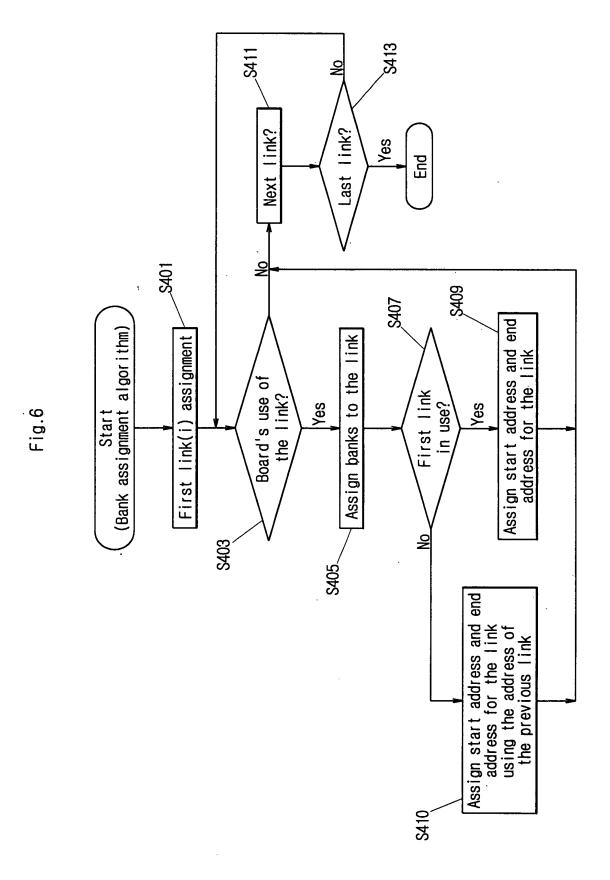


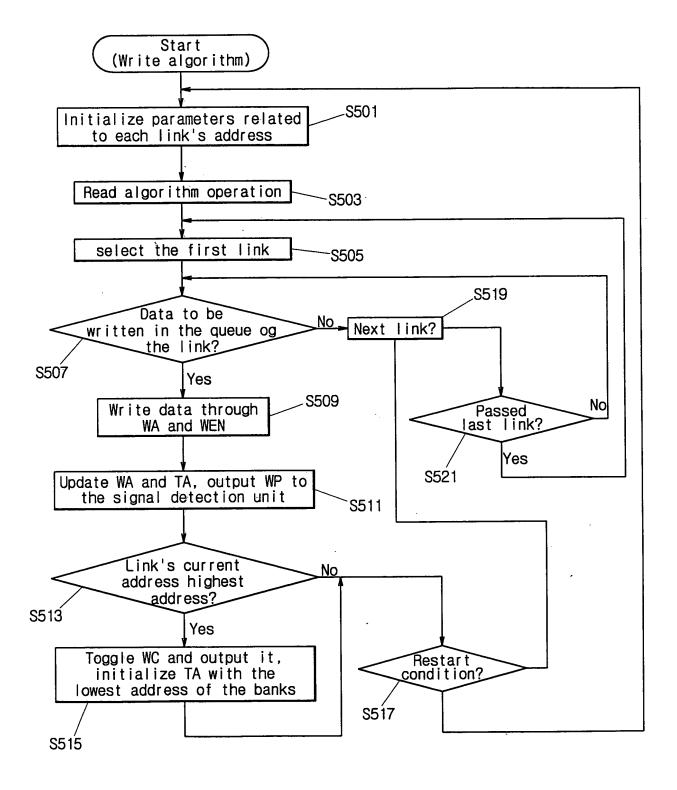
Fig.3



Bank n	•
Bank n-1	
Bank n	
Bank n	
Bank n	
m m-1	0 bit(address)
(MSB)	(LSB)







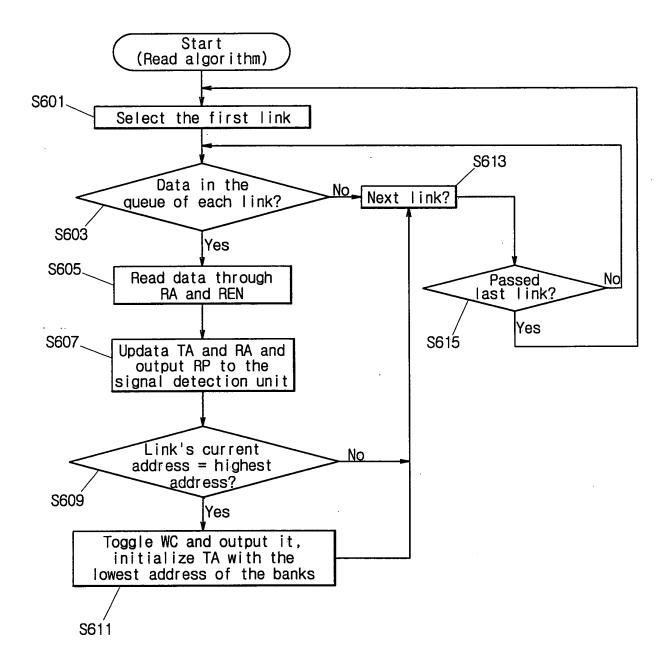


Fig.9

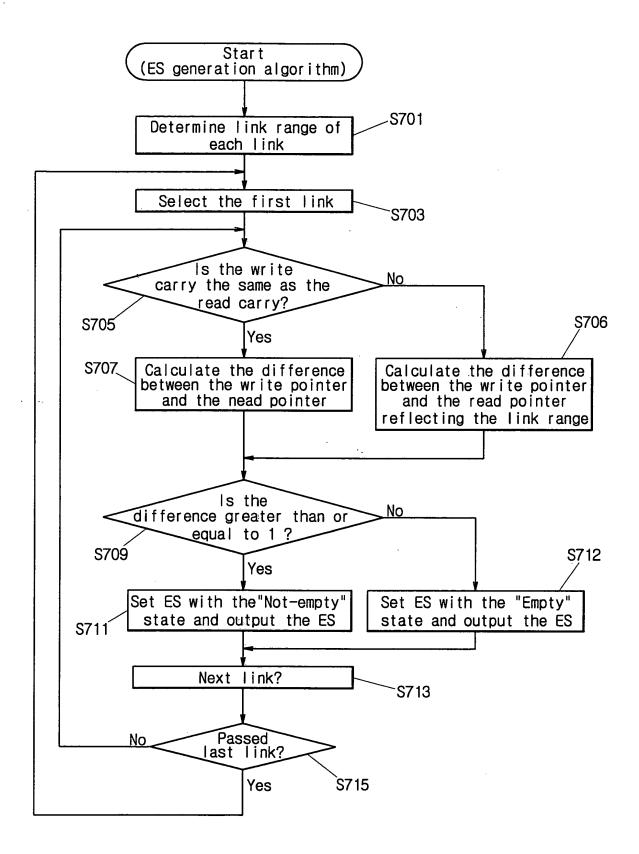


Fig. 10

